

AIT/0810/68/2017-18/CQ/Proj

Date : 14 Dec 17Due date : 31 Dec 17

**CALL FOR QUOTATION : PROCUREMENT OF DSP TRAINER KITS IN
DEPARTMENT OF E&TC**

1. This Institute is interested in procuring DSP Trainig Kits for E&TC Department as per the specifications given below:-

S/No	Description	Qty	Rate (Rs)	GST (Rs)	Amount (Rs)
(a)	Required Specification / or equivalent is attached at Appendix . The following experiments are to be performed by using the kit:- (i) Convolution (ii) Discrete Fourier Transform Using FFT Algorithm (iii) Discrete Fourier Transform Using DFT FFT Radix 2 Algorithm (iv) FIR filter (v) Real time audio signal capture may found useful.	2 Nos			
Total (Inclusive of all taxes)					

2. Please send your lowest quotation in two bid system format (**separate sealed envelops for Technical Bid & Commercial Bid enclosed in a sealed single envelop**) addressed to Director AIT so as to reach this institute on or before 31 Dec 17. Late quotations shall not be considered.

(Contd..2)

3. **Terms and Conditions.**

- (a) The price quoted should be inclusive of taxes and delivery at Army Institute of Technology, Dighi, Pune-411015.
- (b) Discount offered if any, should be indicated as percentage of rates quoted, separately.
- (c) Supply should be done within three weeks of the date of supply order.
- (d) Quotations will be opened by a board of officers detailed by AIT. Technical Specification will be discussed in detail during Technical Evaluation Committee meeting, which will be intimated to you later.
- (e) Payments for supplies will be made direct to the supplier by crossed cheque only after delivery of vehicle and on verification by a board of officers.
- (f) Printed conditions of the tenders/quotations shall not be binding on us.
- (g) The acceptance of quotation will rest with the Director, Army Institute of Technology, Dighi, Pune – 411 015, who does not bind himself to accept the lowest quotation and reserves the right himself to reject or partially accept any or all quotations received without assigning any reason.
- (h) Quotations are liable to be rejected if any of the above conditions are not complied with.
- (j) Kindly quote reference number & subject of this quotation, Name of Firm, address and contact number on the envelope.

(KE Vijayan)
Col (Retd)
Jt Director
for Director

(Contd..3)

REQUIRED SPECIFICATION / OR EQUIVALENT

TMS320C674x Fixed/Floating-Point VLIW DSP Core based development kit

On-Chip Memory:

- Two Level Cache Memory Architecture
- 32K-Byte L1P Program RAM/Cache
- 32K-Byte L1D Data RAM/Cache
- 256K-Byte L2 Unified Mapped RAM/Cache
- 128K-Byte RAM Shared Memory

On-Board Features:

2 Meg x 16 Bit x 4 Banks Synchronous DRAM for External Program/Data

Codec:

- Stereo Audio ADC & DAC 16/20/24/32-Bit Data Format
- Supports Rates From 8 kHz to 96 kHz
- Programmable Input /Output Analog Gains
- Audio Serial Data Bus Supports I2S, Left/Right-Justified, DSP, and TDM Modes

ADC/DAC

ADC:

- No of ADC input : 2 Channels
- Resolution : 12 bit
- Sampling rate: 1Msps (max)
- Analog input range : + 5V (max)
- High speed SPI serial interface

DAC:

- No of DAC output : 2 Channels
- Resolution : 12 bit
- High speed SPI serial interface

Extra Features:

- Provision to connect external JTAG Emulator
- 8 user definable LEDs
- 8 position DIP switch, user definable
- Two 3.5 mm. audio jacks for audio IN and OUT □ Expansion connector for GPIO / Video Add on card

JTAG Emulator(XDS100V1):

Emulation Connect/Disconnect, Read/Write memory, Read registers, Load program, Run, Halt, Step, Software and Hardware Breakpoint support, Real-Time Mode. Support for USB Full Speed (12 Mbits/s)